

**Notice of References Cited**

Application/Control No.

09/505,695

Applicant(s)/Patent Under  
Reexamination  
BERRY ET AL.

Examiner

Thao X Le

Art Unit

2814

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,242,165 ✓	06-2001	Vaartstra, Brian A.	134/1
	B	US-5,578,133 ✓	11-1996	Sugino et al.	134/2
	C	US-5,677,113	10-1997	Suzuki et al.	134/1
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Silicon Processing for the VLSI Era Volume 1 by S. Wolf and R.N. Tauber, 1986, pages 429, 452-453, 514-517
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.